

Simulink® Coverage™

Getting Started



MATLAB® & SIMULINK®

R2020a



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Simulink® Coverage™ Getting Started

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1	Simulink Coverage Product Description	
	Simulink Coverage Product Description	1-2
2	Getting Started with Simulink Coverage	
	Basic Simulink Coverage Workflow	2-2
	Resolving Missing Coverage	2-4
	Recording and Evaluating Coverage	2-5
	Resolving Missing Coverage	2-5
	Reporting and Archiving Results	2-5
	Simulink Coverage in End-to-End Systematic Verification	2-7
	System Requirements	2-9
	Simulation Data	2-9
	Simulation	2-9
	Coverage Reports and Model Highlighting	2-9
	Resolve Missing Coverage	2-10
	Archive and Report	2-10

Simulink Coverage Product Description

Simulink Coverage Product Description

Measure test coverage in models and generated code

Simulink® Coverage™ performs model and code coverage analysis that measures testing completeness in models and generated code. It applies industry-standard metrics such as decision, condition, modified condition/decision coverage (MCDC), and relational boundary coverage to assess the effectiveness of simulation testing in models, software-in-the-loop (SIL), and processor-in-the-loop (PIL). You can use missing coverage data to find gaps in testing, missing requirements, or unintended functionality.

Simulink Coverage produces interactive reports showing how much of your model, C/C++ S-functions, MATLAB® functions, and code generated by Embedded Coder® has been exercised. You can highlight coverage results in blocks and subsystems to visualize gaps in testing. To assess testing completeness, you can accumulate coverage data from multiple test runs, as well as view coverage achieved through unit and system tests. Coverage outcomes can be traced to requirements and tests. You can apply filters to exclude blocks from coverage and justify missing coverage in reports.

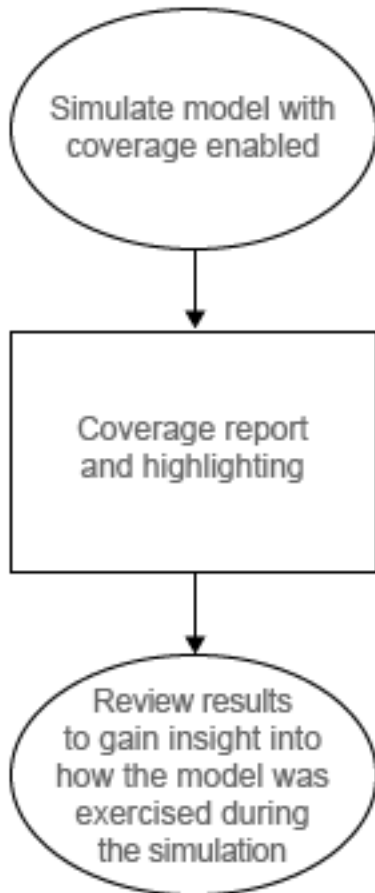
Support for industry standards is available through DO Qualification Kit and IEC Certification Kit.

Getting Started with Simulink Coverage

- “Basic Simulink Coverage Workflow” on page 2-2
- “Resolving Missing Coverage” on page 2-4
- “Simulink Coverage in End-to-End Systematic Verification” on page 2-7

Basic Simulink Coverage Workflow

The simplest use of Simulink Coverage is to gain insight into which parts of your model are exercised during simulation.



After you enable coverage for a model with test cases, Simulink Coverage reports on, and highlights in the model, those objects in your design that were exercised during simulation. You use the coverage reports and model highlighting to learn which parts of your model were active during the specified test cases.

Further steps include “Resolving Missing Coverage” on page 2-4 and, for the most in-depth user workflow, “Simulink Coverage in End-to-End Systematic Verification” on page 2-7.

See Also

More About

- “Create and Run Test Cases”
- “Types of Model Coverage”
- “Types of Code Coverage”

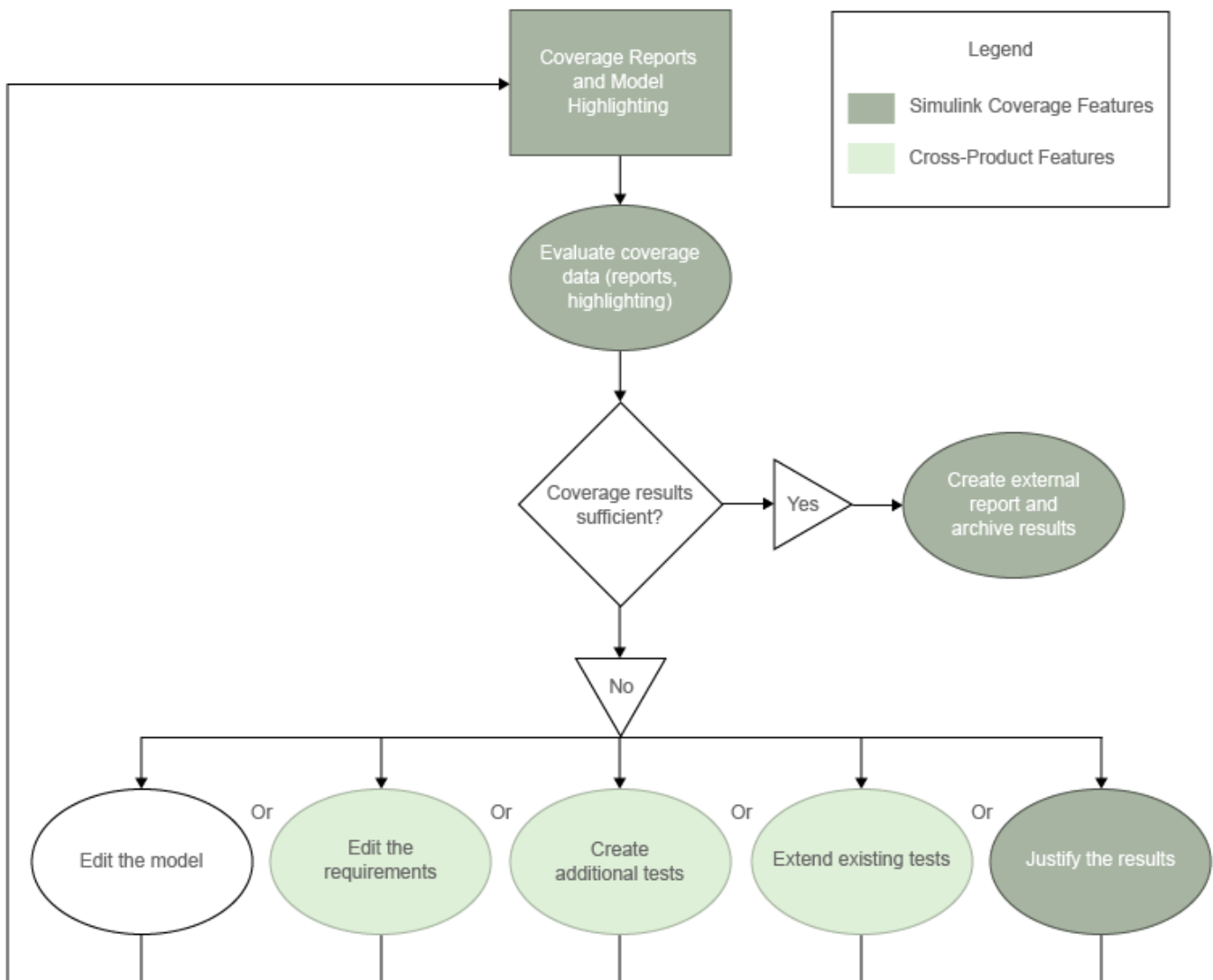
- “View Coverage Results in a Model”

Resolving Missing Coverage

In this section...

“Recording and Evaluating Coverage” on page 2-5
 “Resolving Missing Coverage” on page 2-5
 “Reporting and Archiving Results” on page 2-5

After you generate a model coverage report, you can use Simulink Coverage to help you resolve missing coverage for your model.



In this section...

“Recording and Evaluating Coverage” on page 2-5
 “Resolving Missing Coverage” on page 2-5

In this section...

“Reporting and Archiving Results” on page 2-5

Recording and Evaluating Coverage

After you enable coverage recording and simulate your model, you then evaluate the coverage data to find gaps in your testing. Coverage reports and model highlighting help you to understand which parts of your design are sufficiently exercised by your test cases.

For more information, see:

- “Specify Coverage Options”
- “View Coverage Results in a Model”
- “Types of Coverage Reports”
- “Overview of Model Coverage Highlighting”

Resolving Missing Coverage

If your coverage tests do not sufficiently exercise your model, there are a few steps you can take to increase your coverage:

- **Edit the model** — Your model might contain unintended functionality that is not part of your required design. Remove the unintended functionality.
- **Edit the requirements** — Your requirements might not sufficiently detail the testing required to exercise all parts of your design. You can use Simulink Requirements™ to author and edit requirements from within the Simulink environment. For more information, see “Test Model Against Requirements and Report Results”.
- **Create additional tests** — Your existing tests might not fully exercise the intended simulation inputs according to your requirements. You can use Simulink Coverage to create additional tests to model these inputs. For more information, see “Create Tests with cvtest” and “Generate Test Cases” (Simulink Design Verifier).
- **Extend existing tests** — Your existing tests might not fully exercise all parts of your design. If you have a Simulink Design Verifier™ license, you may be able to automatically generate additional tests to exercise untested parts of your design. For more information, see “Incrementally Increase Test Coverage Using Test Case Generation”.
- **Justify the results** — Parts of your model might not be exercised during simulation according to your design, such as subsystems that only enable during failures. To achieve full coverage in such cases, you can justify the missing coverage. For more information, see “Create, Edit, and View Coverage Filter Rules”.

Reporting and Archiving Results

If your coverage tests sufficiently exercise your model, you can archive the reported results. For more information, see:

- “Top-Level Model Coverage Report”
- “Access, Manage, and Accumulate Coverage Results by Using the Results Explorer”
- “Code Coverage Report”

- “Export Model Coverage Web View”
- “Create HTML Reports with cvhtml”

See Also

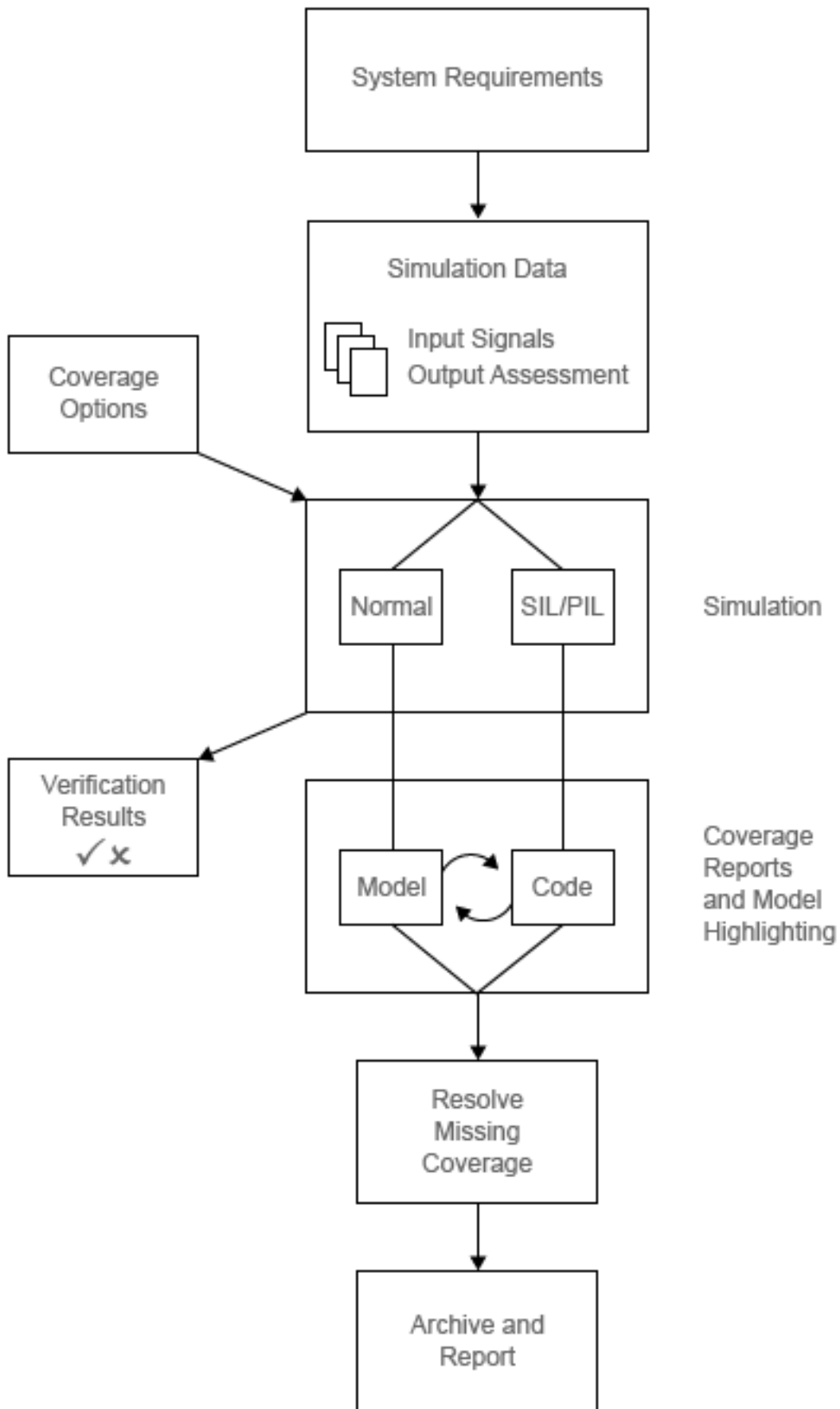
More About

- “Perform Functional Testing and Analyze Test Coverage”

Simulink Coverage in End-to-End Systematic Verification

In this section...
“System Requirements” on page 2-9
“Simulation Data” on page 2-9
“Simulation” on page 2-9
“Coverage Reports and Model Highlighting” on page 2-9
“Resolve Missing Coverage” on page 2-10
“Archive and Report” on page 2-10

You can use Simulink Coverage to increase confidence in your design and testing through end-to-end systematic verification.



In this section...

“System Requirements” on page 2-9

“Simulation Data” on page 2-9

“Simulation” on page 2-9

“Coverage Reports and Model Highlighting” on page 2-9

“Resolve Missing Coverage” on page 2-10

“Archive and Report” on page 2-10

System Requirements

End-to-end systematic verification begins with system requirements. The system requirements detail the model design and the testing and verification specifications.

If you have a Simulink Requirements license, you can author and manage system requirements, link Simulink blocks to requirements, and link test cases to the requirements that they verify from within the Simulink environment. For more information, see “Test Model Against Requirements and Report Results”.

Simulation Data

To record coverage, you first specify input signals for your model. For more information, see “Create and Run Test Cases” and “Perform Functional Testing and Analyze Test Coverage”.

If you have a Simulink Design Verifier license, you can also specify expected output signals for the specified input signals. For more information, see “Perform Functional Testing and Analyze Test Coverage” (Simulink Design Verifier). You can also automatically generate test case for a system. For more information, see “Workflow for Test Case Generation” (Simulink Design Verifier).

Simulation

After you “Specify Coverage Options” for your model, you then simulate the model and record model coverage. For more information on the types of model coverage, see “Types of Model Coverage”. You can record several runs of coverage data and accumulate the results. For more information, see “Access, Manage, and Accumulate Coverage Results by Using the Results Explorer” and “Cumulative Coverage Data”.

If you have an Embedded Coder license, you can also record code coverage for models in software-in-the-loop (SIL) mode and processor-in-the-loop (PIL) mode. The code coverage reports tell you which parts of your generated code are exercised by your tests, and also which elements of your model are associated with which parts of your model. For more information on enabling SIL or PIL code coverage for a model, see “Specify Code Coverage Options”.

If you have a Simulink Design Verifier license, you can model and verify design requirements using property proving. For more information, see “Requirements-Based Verification” (Simulink Design Verifier).

Coverage Reports and Model Highlighting

After you record coverage for a simulation, you then review the coverage reports and model highlighting to identify gaps in your testing, with the goal of gaining confidence that your tests and

verification are complete and adequate. For more information on coverage reports and model highlighting, see:

- “View Coverage Results in a Model”
- “Top-Level Model Coverage Report”
- “Code Coverage Report”
- “Access, Manage, and Accumulate Coverage Results by Using the Results Explorer”

If you have an Embedded Coder license and you record code coverage for models in software-in-the-loop (SIL) mode and processor-in-the-loop (PIL) mode, code coverage reports tell you which parts of your generated code are exercised by your tests, and also which elements of your model are associated with which parts of your model. For more information on enabling SIL or PIL code coverage for a model, see “Code Coverage for Models in Software-in-the-Loop (SIL) Mode and Processor-in-the-Loop (PIL) Mode”.

Resolve Missing Coverage

If you find that parts of your design are not fully tested, you can take steps to resolve the missing coverage and gain confidence that your tests and verification are complete and adequate. For more information, see “Resolving Missing Coverage” on page 2-4.

Archive and Report

When you are confident in the testing and verification of your design, you can archive and report the results of the coverage recording. For more information, see:

- “Top-Level Model Coverage Report”
- “Code Coverage Report”
- “Access, Manage, and Accumulate Coverage Results by Using the Results Explorer”
- “Export Model Coverage Web View”
- “Create HTML Reports with cvhtml”